

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1013	(integrat\$3 with circuit with wireless).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/16 17:15
L2	111	1 and (semiconduct\$3 with chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:16
L3	0	1 and (semiconduct\$3 with chip with multiplicity)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:17
L4	36	2 and (receiv\$3 with wireless with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/16 17:18
L5	56	(integrat\$3 with (circuit chip) with wireless with coupl\$3).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:28
L6	0	5 and (semiconduct\$3 with chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:28
L7	6	5 and (semiconduct\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:29
L8	2	((NARA) near2 (WON)).INV.	US-PGPUB; USPAT	OR	OFF	2008/06/16 17:37
L9	0	7 and (modulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:37

L10	5	5 and (modulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/16 17:37
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